

Syllabus for Pre-Ph. D Examination
Electronics and Communication Engineering

PAPER-I		Subject Code
S.No	Subject	
1	DIGITAL SYSTEM DESIGN	PH2404101
2	EMBEDDED REAL TIME OPERATING SYSTEMS	PH2404102
3	ADVANCED DIGITAL SIGNAL PROCESSING	PH2404103
4	DIGITAL DATA COMMUNICATIONS	PH2404104
5	INFORMATION THEORY AND CODING TECHNIQUES	PH2404105
6	TRANSFORM TECHNIQUES	PH2404106
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13	CMOS ANALOG AND DIGITAL IC DESIGN	PH2404113
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15	NETWORK SECURITY AND CRYPTOGRAPHY	PH2404115

PAPER-II		Subject Code
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1	EMBEDDED SYSTEM DESIGN	PH2404201
2	WIRELESS COMMUNICATIONS AND NETWORKS	PH2404202
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15	MICRO ELECTRO MECHANICAL SYSTEM DESIGN	PH2404215

PAPER – I

(PH2404101)DIGITAL SYSTEM DESIGN

UNIT-I: Minimization Procedures and CAMP Algorithm

Review on minimization of switching functions using tabular methods, k-map, QM algorithm, CAMP-I algorithm, Phase-I: Determination of Adjacencies, DA, CSC, SSMs and EPCs,, CAMP-I algorithm, Phase-II: Passport checking, Determination of SPC, CAMP-II algorithm: Determination of solution cube, Cube based operations, determination of selected cubes are wholly within the given switching function or not, Introduction to cube based algorithms.

UNIT-II: PLA Design, PLA Minimization and Folding Algorithms

Introduction to PLDs, basic configurations and advantages of PLDs, PLA-Introduction, Block diagram of PLA, size of PLA, PLA design aspects, PLA minimization algorithm(IISc algorithm), PLA folding algorithm(COMPACT algorithm)-Illustration of algorithms with suitable examples.

UNIT -III: Design of Large Scale Digital Systems

Algorithmic state machine charts-Introduction, Derivation of SM Charts, Realization of SM Chart, control implementation, control unit design, data processor design, ROM design, PAL design aspects, digital system design approaches using CPLDs, FPGAs and ASICs.

UNIT-IV: Fault Diagnosis in Combinational Circuits

Faults classes and models, fault diagnosis and testing, fault detection test, test generation, testing process, obtaining a minimal complete test set, circuit under test methods- Path sensitization method, Boolean difference method, properties of Boolean differences, Kohavi algorithm, faults in PLAs, DFT schemes, built in self-test.

UNIT-V: Fault Diagnosis in Sequential Circuits

Fault detection and location in sequential circuits, circuit test approach, initial state identification, Haming experiments, synchronizing experiments, machine identification, distinguishing experiment, adaptive distinguishing experiments.

TEXT BOOKS:

1. Logic Design Theory-N. N. Biswas, PHI
2. Switching and Finite Automata Theory-Z. Kohavi , 2nd Edition, 2001, TMH
3. Digital system Design using PLDd-Lala

REFERENCE BOOKS:

1. Fundamentals of Logic Design – Charles H. Roth, 5th Ed., Cengage Learning.
2. Digital Systems Testing and Testable Design – Miron Abramovici, Melvin A. Breuer and Arthur D. Friedman- John Wiley & Sons Inc.

**Syllabus for Pre-Ph. D Examination
Electronics and Communication Engineering**

(PH2404102)Embedded Real Time Operating Systems

UNIT-I: Introduction

OS Services, Process Management, Timer Functions, Event Functions, Memory Management, Device, File and IO Systems Management, Interrupt Routines in RTOS Environment and Handling of Interrupt Source Calls, Real-Time Operating Systems, Basic Design Using an RTOS, RTOS Task Scheduling Models, Interrupt Latency and Response of the Tasks as Performance Metrics, OS Security Issues.

UNIT-II: RTOS Programming

Basic Functions and Types of RTOS for Embedded Systems, RTOS mCOS-II, RTOS Vx Works, Programming concepts of above RTOS with relevant Examples, Programming concepts of RTOS Windows CE, RTOS OSEK, RTOS Linux 2.6.x and RTOS RT Linux.

UNIT-III: Program Modeling – Case Studies

Case study of embedded system design and coding for an Automatic Chocolate Vending Machine (ACVM) Using Mucos RTOS, case study of digital camera hardware and software architecture, case study of coding for sending application layer byte streams on a TCP/IP Network Using RTOS Vx Works, Case Study of Embedded System for an Adaptive Cruise Control (ACC) System in Car, Case Study of Embedded System for a Smart Card, Case Study of Embedded System of Mobile Phone Software for Key Inputs.

UNIT-IV: Target Image Creation & Programming in Linux

Off-The-Shelf Operating Systems, Operating System Software, Target Image Creation for Window XP Embedded, Porting RTOS on a Micro Controller based Development Board. Overview and programming concepts of Unix/Linux Programming, Shell Programming, System Programming.

UNIT-V: Programming in RT Linux

Overview of RT Linux, Core RT Linux API, Program to display a message periodically, semaphore management, Mutex, Management, Case Study of Appliance Control by RT Linux System.

TEXT BOOKS:

1. Dr. K.V.K.K. Prasad: “Embedded/Real-Time Systems” Dream Tech Publications, Black pad book.
2. Rajkamal: “Embedded Systems-Architecture, Programming and Design”, Tata McGraw Hill Publications, Second Edition, 2008.

REFERENCES:

1. Labrosse, “Embedding system building blocks “, CMP publishers.2007
2. Rob Williams,” Real time Systems Development”, Butterworth Heinemann Publications.2005.

(PH2404103)ADVANCED DIGITAL SIGNAL PROCESSING

UNIT –I:

Review of DFT, FFT, IIR Filters and FIR Filters:

Multi Rate Signal Processing: Introduction, Decimation by a factor D, Interpolation by a factor I, Sampling rate conversion by a rational factor I/D, Multistage Implementation of Sampling Rate Conversion, Filter design & Implementation for sampling rate conversion.

UNIT –II:

Applications of Multi Rate Signal Processing:

Design of Phase Shifters, Interfacing of Digital Systems with Different Sampling Rates, Implementation of Narrow Band Low Pass Filters, Implementation of Digital Filter Banks, Sub-band Coding of Speech Signals, Quadrature Mirror Filters, Transmultiplexers, Over Sampling A/D and D/A Conversion.

UNIT -III:

Non-Parametric Methods of Power Spectral Estimation: Estimation of spectra from finite duration observation of signals, Non-parametric Methods: Bartlett, Welch & Blackman-Tukey methods, Comparison of all Non-Parametric methods

UNIT –IV:

Implementation of Digital Filters:

Introduction to filter structures (IIR & FIR), Frequency sampling structures of FIR, Lattice structures, Forward prediction error, Backward prediction error, Reflection coefficients for lattice realization, Implementation of lattice structures for IIR filters, Advantages of lattice structures.

UNIT –V:

Parametric Methods of Power Spectrum Estimation: Autocorrelation & Its Properties, Relation between auto correlation & model parameters, AR Models - Yule-Walker & Burg Methods, MA & ARMA models for power spectrum estimation, Finite word length effect in IIR digital Filters – Finite word-length effects in FFT algorithms.

TEXT BOOKS:

1. Digital Signal Processing: Principles, Algorithms & Applications - J.G.Proakis & D. G. Manolakis, 4th Ed., PHI.
2. Discrete Time Signal Processing - Alan V Oppenheim & R. W Schaffer, PHI.
3. DSP – A Practical Approach – Emmanuel C. Ifeache, Barrie. W. Jervis, 2 Ed., Pearson Education.

REFERENCE BOOKS:

1. Modern Spectral Estimation: Theory & Application – S. M .Kay, 1988, PHI.
2. Multi Rate Systems and Filter Banks – P.P.Vaidyanathan – Pearson Education.
3. Digital Signal Processing – S.Salivahanan, A.Vallavaraj, C.Gnanapriya, 2000,TMH
4. Digital Spectral Analysis – Jr. Marple

(PH2404104)DIGITAL DATA COMMUNICATIONS

UNIT -I:

Digital Modulation Schemes:

BPSK, QPSK, 8PSK, 16PSK, 8QAM, 16QAM, DPSK – Methods, Band Width Efficiency, Carrier Recovery, Clock Recovery.

UNIT -II:

Basic Concepts of Data Communications, Interfaces and Modems:

Data Communication Networks, Protocols and Standards, UART, USB, I2C, I2S, Line Configuration, Topology, Transmission Modes, Digital Data Transmission, DTE-DCE interface, Categories of Networks – TCP/IP Protocol suite and Comparison with OSI model.

UNIT -III:

Error Correction: Types of Errors, Vertical Redundancy Check (VRC), LRC, CRC, Checksum, Error Correction using Hamming code

Data Link Control: Line Discipline, Flow Control, Error Control

Data Link Protocols: Asynchronous Protocols, Synchronous Protocols, Character Oriented Protocols, Bit-Oriented Protocol, Link Access Procedures.

UNIT -IV:

Multiplexing: Frequency Division Multiplexing (FDM), Time Division Multiplexing (TDM), Multiplexing Application, DSL.

Local Area Networks: Ethernet, Other Ether Networks, Token Bus, Token Ring, FDDI.

Metropolitan Area Networks: IEEE 802.6, SMDS

Switching: Circuit Switching, Packet Switching, Message Switching.

Networking and Interfacing Devices: Repeaters, Bridges, Routers, Gateway, Other Devices.

UNIT -V:

Multiple Access Techniques:

Random Access, Aloha- Carrier Sense Multiple Access (CSMA)- Carrier Sense Multiple Access with Collision Avoidance (CSMA/CA), Controlled Access- Reservation- Polling- Token Passing, Channelization, Frequency- Division Multiple Access (FDMA), Time - Division Multiple Access (TDMA), Code - Division Multiple Access (CDMA), OFDM and OFDMA.

TEXT BOOKS:

1. Data Communication and Computer Networking - B. A.Forouzan, 2nd Ed., 2003, TMH.
2. Advanced Electronic Communication Systems - W. Tomasi, 5th Ed., 2008, PEI.

REFERENCE BOOKS:

1. Data Communications and Computer Networks - Prakash C. Gupta, 2006, PHI.
2. Data and Computer Communications - William Stallings, 8th Ed., 2007, PHI.
3. Data Communication and Tele Processing Systems -T. Housely, 2nd Ed, 2008, BSP.
4. Data Communications and Computer Networks- Brijendra Singh, 2nd Ed., 2005, PHI.

(PH2404105)INFORMATION THEORY AND CODING TECHNIQUES

UNIT I

INFORMATION THEORY AND SOURCE CODING

Uncertainty, information, entropy and its properties, entropy of binary memoryless source and its extension to discrete memoryless source, source coding theorem, data compression, prefix coding, Huffman coding, Lempel-Ziv coding, Source with memory and its entropy.

UNIT II

DISCRETE CHANNELS

Binary Symmetric Channel, mutual information & its properties, Channel capacity, channel coding theorem and its application to BSC, Shannon's theorem on channel capacity, capacity of a channel of infinite bandwidth, bandwidth - S/N trade off, practical communication systems in light of Shannon's theorem, Fading channel, channels with memory.

UNIT III

GROUPS, FIELDS AND LINEAR BLOCK CODES

Galois field and its construction in $GF(2^m)$ and its basic properties, vector spaces and matrices in $GF(2)$, Linear block codes, systematic codes and its encoding circuit, syndrome and error detection, minimum distance, error detecting and correcting capabilities of block code, decoding circuit, probability of undetected error for linear block code in BSC, Hamming code and their applications.

UNIT IV

CYCLIC CODES AND BCH CODES

Basic properties of Cyclic codes, Generator and parity check matrix of cyclic codes, encoding and decoding circuits, syndrome computation and error detection, cyclic Hamming codes, encoding and decoding of BCH codes, error location and correction.

UNIT V

CONVOLUTIONAL CODES

Introduction to convolution code, its construction and Viterbi algorithm for maximum likelihood decoding. Automatic repeat request strategies and their throughput efficiency considerations.

Text Books:

1. Digital and Analogue Communication Systems. K. Sam Shanmugam, Jhon Wiely Inada Pvt.Ltd,1996
2. Digital Communications, Simon Haykin, Jhon Wiley India Pvt. Ltd,2008
3. Error Correction Coding, Todd K Moon.Wiley Stsd.Edition,2006

Reference Books

1. Information Theory and Coding Techniques by Dr.S.S.K.Iyer, Published by Prentice Hall of India, First Edition:2004
2. Information Thery, Cosing and Cryptography, by Ranjan Bose published by McGraw-Hill Education, Second Edition:2009
3. Lathi B. P., Modern Analog and Digital Communication Systems, Oxford Univ. Press
4. Shu Lin and Costello, Error Control Coding : Fundamentals and Applications, 2nd Edition, Pearson.
5. Sklar, Digital Communication, Pearson Education Asia.
6. Haykin Simon, Digital Communication, Wiley Publ.
7. Proakis, Digital Communication, McGraw Hill.
8. Schaum's Outline Series, Analog and Digital Communication, TMH.

(PH2404106)TRANSFORM TECHNIQUES

UNIT -I:

Fourier Analysis:

Fourier series, Examples, Fourier Transform, Properties of Fourier Transform, Examples of Fourier transform, sampling theorem, Partial sum and Gibbs phenomenon, Fourier analysis of Discrete time Signals, Discrete Fourier Transform.

Time – Frequency Analysis: Window function, Short Time Fourier Transform, Discrete Short Time Fourier Transform, Continuous wavelet transform, Discrete wavelet transform, wavelet series, Interpretations of the Time-Frequency plot.

UNIT -II:

Transforms:

Walsh, Hadamard, Haar and Slant Transforms, DCT, DST, KLT, Singular value Decomposition – definition, properties and applications

UNIT -III:

Continuous Wavelet Transform (CWT):

Shortcomings of STFT, Need for wavelets, Wavelet Basis- Concept of Scale and its relation with frequency, Continuous time wavelet Transform Equation- Series Expansion using Wavelets- CWT- Tiling of time scale plane for CWT. Important Wavelets: Haar, Mexican Hat, Meyer, Shannon, Daubechies.

UNIT -IV:

Multi Rate Analysis and DWT:

Need for Scaling function – Multi Resolution Analysis, Two-Channel Filter Banks, Perfect Reconstruction Condition, Relationship between Filter Banks and Wavelet Basis, DWT, Structure of DWT Filter Banks, Daubechies Wavelet Function, Applications of DWT.

UNIT -V:

Wavelet Packets and Lifting: Wavelet Packet Transform, Wavelet packet algorithms, Thresholding-Hard thresholding, Soft thresholding, Multidimensional Wavelets, Bi-orthogonal basis- B-Splines, Lifting Scheme of Wavelet Generation, Multi Wavelets

TEXT BOOKS:

1. A Wavelet Tour of Signal Processing theory and applications -Raghuveer M.Rao and Ajit S. Bopardikar, Pearson Edu, Asia, New Delhi, 2003.
2. K.P.Soman and K.I Ramachandran, “ Insight into Wavelets – from theory to practice” PHI, Second edition,2008

REFERENCE BOOKS:

1. Fundamentals of Wavelets- Theory, Algorithms and Applications -Jaideva C Goswami, Andrew K Chan, John Wiley & Sons, Inc, Singapore, 1999.
2. Jaideva C.Goswami and Andrew K.Chan, “ Fundamentals of Wavelets” Wiley publishers, 2006
3. A Wavelet Tour of Signal Processing-Stephen G. Mallat, Academic Press, 2 Ed
4. Digital Image Processing – S.Jayaraman, S.Esakirajan, T.Veera Kumar – TMH,2009

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY GURAJADA VIZIANAGARAM
Syllabus for Pre-Ph. D Examination
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(PH2404107)BIOMEDICAL SIGNAL PROCESSING

UNIT –I:

Random Processes:

Stationary random process, Ergodicity, Power spectral density and autocorrelation function of random processes. Noise power spectral density analysis, Noise bandwidth and noise figure of systems.

UNIT -II:

Data Compression Techniques:

Lossy and Lossless data reduction Algorithms, ECG data compression using Turning point, AZTEC, CORTES, Huffman coding, vector quantisation, DICOM Standards

UNIT -III:

Cardiological Signal Processing:

Pre-processing, QRS Detection Methods, Rhythm analysis, Arrhythmia Detection Algorithms, Morphological Analysis of ECG, Artifacts in ECG
Automated ECG Analysis, , ECG Pattern Recognition Analysis,
ECG Pattern Recognition.

Adaptive Noise Cancelling: Principles of Adaptive Noise Cancelling, Adaptive Noise Cancelling with the LMS Adaptation Algorithm, Noise Cancelling Method to Enhance ECG Monitoring, Fetal ECG Monitoring.

UNIT -IV:

Signal Averaging, Polishing: Mean and trend removal, Prony's method, Prony's Method based on the Least Squares Estimate, Linear prediction, Yule – Walker (Y –W) equations, Analysis of Evoked Potentials.

UNIT -V:

Neurological Signal Processing:

Modelling of EEG Signals, Detection of spikes and spindles Detection of Alpha, Beta and Gamma Waves, Auto Regressive (A.R.) modelling of seizure EEG, Sleep Stage analysis, Inverse Filtering, Least squares and polynomial modelling.

TEXT BOOKS:

1. Probability, Random Variables & Random Signal Principles – Peyton Z. Peebles, 4th Ed., 2009, TMH.
2. Biomedical Signal Processing- Principles and Techniques - D. C. Reddy, 2005, TMH.

REFERENCE BOOKS:

1. Digital Bio signal Processing - Weitkunat R, 1991, Elsevier.
2. R M Rangayyan “Biomedical Signal Analysis: A case Based Approach”, IEEE Press, John Wiley & Sons. Inc, 2002
3. Biomedical Signal Processing - Akay M , IEEE Press.
4. Biomedical Signal Processing -Vol. I Time & Frequency Analysis - Cohen.A, 1986, CRC Press.
5. Biomedical Digital Signal Processing: C-Language Experiments and Laboratory Experiments, Willis J. Tompkins, PHI.

(PH2404108)DATA ACQUISITION SYSTEMS

UNIT-1

INTRODUCTION: Objective of a DAS, single channel DAS, Multi-channel DAS, Components used in DAS– Converter Characteristics-Resolution-Non-linearity, settling time, Monotonicity.

UNIT-2

ANALOG TO DIGITAL CONVERTERS (ADCS): Classification of A/D converters.Parallel feed back – Successive approximation – Ramp comparison – Dual slope integration – Voltage to frequency – Voltage to Time – Logarithmic types of ADCS.

NON-LINEAR DATA CONVERTERS (NDC): Basic NDC configurations – Some common NDACS and NADCS – Programmable non-linear ADCS – NADC using optimal sized ROM – High speed hybrid NADC – PLS based NADC – Switched capacitor NDCS.

ADC APPLICATIONS: Data Acquisition systems – Digital signal processing systems – PCM voice communication systems – Test and measurement instruments – Electronic weighing machines.

UNIT-3

DIGITAL TO ANALOG CONVERTERS (DACS): Principles and design of – Parallel R– 2R, Weighted resistor, inverted ladder, D/A decoding – Codes other than ordinary binary.

DATA CONVERTER APPLICATIONS: DAC applications – Digitally programmable V/I sources – Arbitrary waveform generators – Digitally programmable gain amplifiers – Analog multipliers/ dividers – Analog delay lines.

UNIT-4

Monolithic data converters: typical study of monolithic DACS and ADCS. Interfacing of DACS and ADCS to a μ P.

UNIT-5

Error budget of DACS and ADCS: Error sources, error reduction and noise reduction techniques in DAS. Error budget analysis of DAS, case study of a DAC and an ADC.

TEXT BOOKS:

1. Electronic Data Converters : Fundamentals and Applications” by Dinesh K.Anvekar and B.S.Sonde, Published by Tata McGraw-Hill in 2007. It is the First edition of the book
2. Electronic Analog/Digital Conversions”, by Hermann Schmid, Published by Tata McGraw-Hill, 1984, First Edition
3. Electronic data converters fundamentals and applications – Dinesh K. Anvekar, B.S. Sonde – Tata McGraw Hill.1994

REFERENCES:

1. E.R.Kanatack, User’s Handbook of D/A and A/D Conveters” by E.R.Hanateck, Published by Wiley in 1982, First Edition.
2. Electronic instrumentation by HS Kalsi-TMH 2nd Edition, 2004
3. Electronic Analog/ Digital conversions – Hermann Schmid – Tata McGraw Hill.1970
4. E.R. Hanateck, User’s Handbook of D/A and A/D converters – Wiley1976
5. Electronic instrumentation by HS Kalsi- TMH 2nd Edition, 2004.
6. Data converters by G.B. Clayton, 1982

(PH2404109)ADVANCED ANTENNA THEORY AND DESIGN

UNIT -I:

Antenna Theory:

Antennas, Radiation concept, Types of Antennas, Antenna parameters, Friis Transmission equation.

UNIT -II:

Aperture Antenna:

Introduction, Pyramidal Horns- Design Procedure, Conical and Corrugated Horns, Aperture Corrugated Horns, Reflected Antennas- Parameters, Analysis of front-fed parabolic reflector, Feed methods and feed types, Cassegrain Reflector Horns.

UNIT -III:

Microstrip Radiators:

Introduction, Rectangular Microstrip Antenna analysis and Design, Circular Microstrip Antenna Analysis and Design,

UNIT -IV:

Microstrip Slot Antennas:

Wave guide fed slots, Radiational mechanism, Micro strip slot antennas, Introduction rectangular slot antennas, narrow, wide, tapered and circularly polarized slot antennas, Annular slot antennas, Comparison of microstrip slot antennas with patch antennas.

UNIT -V:

Micro Strip Antenna Arrays:

Introduction, Micro strip array antennas, Characteristics of fixed beam linear antenna arrays, Linear micro strip arrays, Characteristics of planar arrays, Microstrip planar arrays, Microstrip scanned array antennas, Phase scanned microstrip arrays, Time delay scanning, Electronic feed switching, Frequency scanned microstrip arrays, Advantage and disadvantages of phased array antennas.

TEXT BOOKS:

1. Constantine Balanis. A - „Antenna Theory-Analysis and Design“, 3rd Edition, John Wiley, 2005.
2. Bahl IJ, and Bhartia -NMicrostrip Antennas, Artech House, 1982.

REFERENCE BOOKS:

1. Microstrip Antenna Design Hand Book -Ramesh Garg, Prakash Bhatia, Architect House Inc. 2001.
2. Samuel Silve - Microwave Antenna - Theory and design, IEE Press, 1984.
3. James.J R. Hall, P S. Wood.C. - Micro strip Antenna-Theory and Design, PeterPeregrinu,1981.

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(PH2404110)MICROWAVE INTEGRATED CIRCUITS

UNIT I

MIC Technology – Thick film and Thin film technology, Hybrid MIC's, Monolithic MIC technology.

UNIT II

Analysis of stripline and microstripline, Method of conformal Transformation, Characteristic parameters of strip, Microstrip lines, Microstrip Circuit Design, Impedance transformers, Filters, Lumped constant Microstrip circuits.

UNIT III

Coupled Microstrips and Directional couplers, Even and odd mode analysis, Theory of coupled microstrip Directional couplers, Calculations for a coupled pair of Microstrips, Branch line couplers.

UNIT IV

Lumped Elements for MIC's Design and fabrication of lumped elements, circuits using lumped elements.

UNIT V

Nonreciprocal components for MIC's Microstrip on Ferrimagnetic substrates, Microstrip circulators. Isolators and phase shifters, Design of microstrip circuits – high power and low power circuits.

TEXT BOOKS:

1. Gupta KC and Amarjit Singh - Microwave Integrated circuits, Wiley Eastern, 1974.
2. Leo Young - Advances in Microwaves, Academic Press.2013

REFERENCE BOOKS:

1. Bharathi Bhat,and S.K. Koul -"Stripline-like Transmission Lines for Microwave Integrated Circuits, New Age International, 2007.

(PH2404111)SMART ANTENNAS

UNIT -I:

Smart Antennas:

Introduction, Need for Smart Antennas, Overview, Smart Antenna Configurations, Switched-Beam Antennas, Adaptive Antenna Approach, Space Division Multiple Access (SDMA), Architecture of a Smart Antenna System, Receiver, Transmitter, Benefits and Drawbacks, Basic Principles, Mutual Coupling Effects.

UNIT -II:

DOA Estimation Fundamentals:

Introduction, Array Response Vector, Received Signal Model, Subspace-Based Data Model, Signal Autocovariance, Conventional DOA Estimation Methods, Conventional Beamforming Method, Capon's Minimum Variance Method, Subspace Approach to DOA Estimation, MUSIC Algorithm, ESPRIT Algorithm, Uniqueness of DOA Estimates .

UNIT -III:

Beam Forming Fundamentals:

Classical Beam former, Statistically Optimum Beamforming Weight Vectors, Maximum SNR Beam former, Multiple Sidelobe Canceller and Maximum, SINR Beam former, Minimum Mean Square Error (MMSE), Direct Matrix Inversion (DMI), Linearly Constrained Minimum Variance (LCMV), Adaptive Algorithms for Beamforming

UNIT -IV:

Integration and Simulation of Smart Antennas:

Overview, Antenna Design, Mutual Coupling, Adaptive Signal Processing Algorithms, DOA, Adaptive Beam forming, Beam forming and Diversity Combining for Rayleigh-Fading, Channel, Trellis-Coded Modulation (TCM) for Adaptive Arrays, Smart Antenna Systems for Mobile Ad Hoc Networks (MANETs), Protocol, Simulations, Discussion.

UNIT -V:

Space-Time Processing:

Introduction, Discrete Space-Time Channel and Signal Models, Space-Time Beamforming, Intersymbol and Co-Channel Suppression, Space-Time Processing for DS-CDMA, Capacity and Data Rates in MIMO Systems, Discussion.

TEXT BOOKS:

1. „Introduction to Smart Antennas“ - Constantine A. Balanis & Panayiotis I. Ioannides, Morgan & Claypool Publishers“ series-2007
2. Joseph C. Liberti Jr., Theodore S Rappaport - “Smart Antennas for Wireless Communications IS-95 and Third Generation CDMA Applications”, PTR – PH publishers, 1st Edition, 1989.

REFERENCE BOOKS:

1. T.S Rappaport - “Smart Antennas Adaptive Arrays Algorithms and Wireless Position Location”, IEEE press 1998, PTR – PH publishers 1999.
Smart Antennas - Lal Chand Godara, CRC Press, LLC-2004.

(PH2404112)LOW POWER VLSI DESIGN

UNIT-I: Fundamentals of Low Power VLSI Design

Need for Low Power Circuit Design, Sources of Power Dissipation – Switching Power Dissipation, Short Circuit Power Dissipation, Leakage Power Dissipation, Glitching Power Dissipation, Short Channel Effects –Drain Induced Barrier Lowering and Punch Through, Surface Scattering, Velocity Saturation, Impact Ionization, Hot Electron Effect.

UNIT-II: Low-Power Design Approaches

Low-Power Design through Voltage Scaling – VTCMOS circuits, MTCMOS circuits, Architectural Level Approach –Pipelining and Parallel Processing Approaches.

Switched Capacitance Minimization Approaches

System Level Measures, Circuit Level Measures, Mask level Measures.

UNIT-III: Low-Voltage Low-Power Adders

Introduction, Standard Adder Cells, CMOS Adder's Architectures – Ripple Carry Adders, Carry Look-Ahead Adders, Carry Select Adders, Carry Save Adders, Low-Voltage Low-Power Design Techniques –Trends of Technology and Power Supply Voltage, Low-Voltage Low-Power Logic Styles.

UNIT-IV: Low-Voltage Low-Power Multipliers

Introduction, Overview of Multiplication, Types of Multiplier Architectures, Braun Multiplier, Baugh-Wooley Multiplier, Booth Multiplier, Introduction to Wallace Tree Multiplier.

UNIT-V: Low-Voltage Low-Power Memories

Basics of ROM, Low-Power ROM Technology, Future Trend and Development of ROMs, Basics of SRAM, Memory Cell, Precharge and Equalization Circuit, Low-Power SRAM Technologies, Basics of DRAM, Self-Refresh Circuit, Future Trend and Development of DRAM.

TEXT BOOKS:

1. CMOS Digital Integrated Circuits – Analysis and Design – Sung-Mo Kang, Yusuf Leblebici, TMH, 2011.
2. Low-Voltage, Low-Power VLSI Subsystems – Kiat-Seng Yeo, Kaushik Roy, TMH Professional Engineering.

REFERENCE BOOKS:

1. Low Power CMOS Design – AnanthaChandrakasan, IEEE Press/Wiley International, 1998.
2. Low Power CMOS VLSI Circuit Design – Kaushik Roy, Sharat C. Prasad, John Wiley & Sons, 2000.
3. Practical Low Power Digital VLSI Design – Gary K. Yeap, Kluwer Academic Press, 2002.
4. Low Power CMOS VLSI Circuit Design – A. Bellamour, M. I. Elamasri, Kluwer Academic Press, 1995.

(PH2404113)CMOS ANALOG AND DIGITAL IC DESIGN

UNIT-I:

MOS Devices and Modeling

The MOS Transistor, Passive Components- Capacitor & Resistor, Integrated circuit Layout, CMOS Device Modeling - Simple MOS Large-Signal Model, Other Model Parameters, Small-Signal Model for the MOS Transistor, Computer Simulation Models, Sub-threshold MOS Model.

MOS Design

Pseudo NMOS Logic – Inverter, Inverter threshold voltage, Output high voltage, Output Low voltage, Gain at gate threshold voltage, Transient response, Rise time, Fall time, Pseudo NMOS logic gates, Transistor equivalency, CMOS Inverter logic.

UNIT-II:

Combinational MOS Logic Circuits:

MOS logic circuits with NMOS loads, Primitive CMOS logic gates – NOR & NAND gate, Complex Logic circuits design – Realizing Boolean expressions using NMOS gates and CMOS gates , AOI and OIA gates, CMOS full adder, CMOS transmission gates, Designing with Transmission gates.

Sequential MOS Logic Circuits

Behaviour of bistable elements, SR Latch, Clocked latch and flip flop circuits, CMOS D latch and edge triggered flip-flop.

UNIT -III:

Dynamic Logic Circuits

Basic principle, Voltage Bootstrapping, Synchronous dynamic pass transistor circuits, Dynamic CMOS transmission gate logic, High performance Dynamic CMOS circuits.

Semiconductor Memories

Types, RAM array organization, DRAM – Types, Operation, Leakage currents in DRAM cell and refresh operation, SRAM operation Leakage currents in SRAM cells, Flash Memory- NOR flash and NAND flash.

UNIT -IV:

Analog CMOS Sub-Circuits

MOS Switch, MOS Diode, MOS Active Resistor, Current Sinks and Sources, Current Mirrors-Current mirror with Beta Helper, Degeneration, Cascode current Mirror and Wilson Current Mirror, Current and Voltage References, Band gap Reference.

UNIT-V:

CMOS Amplifiers

Inverters, Differential Amplifiers, Cascode Amplifiers, Current Amplifiers, Output Amplifiers, High Gain Amplifiers Architectures.

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CMOS Operational Amplifiers

Design of CMOS Op Amps, Compensation of Op Amps, Design of Two-Stage Op Amps, Power- Supply Rejection Ratio of Two-Stage Op Amps, Cascode Op Amps, Measurement Techniques of OP Amp.

TEXT BOOKS:

1. Digital Integrated Circuit Design – Ken Martin, Oxford University Press, 2011.
2. CMOS Digital Integrated Circuits Analysis and Design – Sung-Mo Kang, Yusuf Leblebici, TMH, 3rd Ed., 2011.
3. CMOS Analog Circuit Design - Philip E. Allen and Douglas R. Holberg, Oxford University Press, International Second Edition/Indian Edition, 2010.
4. Analysis and Design of Analog Integrated Circuits- Paul R. Gray, Paul J. Hurst, S. Lewis and R. G. Meyer, Wiley India, Fifth Edition, 2010.

REFERENCE BOOKS:

1. Analog Integrated Circuit Design- David A. Johns, Ken Martin, Wiley Student Edn, 2013.
2. Design of Analog CMOS Integrated Circuits- Behzad Razavi, TMH Edition.
3. CMOS: Circuit Design, Layout and Simulation- Baker, Li and Boyce, PHI.
4. Digital Integrated Circuits – A Design Perspective, Jan M. Rabaey, Anantha Chandrakasan, Borivoje Nikolic, 2nd Ed., PHI.

(PH2404114)VLSI SIGNAL PROCESSING

UNIT-I:

Introduction to DSP

Typical DSP algorithms, DSP algorithms benefits, Representation of DSP algorithms

Pipelining and Parallel Processing

Introduction, Pipelining of FIR Digital filters, Parallel Processing, Pipelining and Parallel Processing for Low Power

Retiming

Introduction – Definitions and Properties – Solving System of Inequalities – Retiming Techniques

UNIT-II:

Folding: Introduction -Folding Transform - Register minimization Techniques – Register minimization in folded architectures – folding of multirate systems

Unfolding: Introduction – An Algorithm for Unfolding – Properties of Unfolding – critical Path, Unfolding and Retiming – Applications of Unfolding

UNIT-III:

Systolic Architecture Design

Introduction – Systolic Array Design Methodology – FIR Systolic Arrays – Selection of Scheduling Vector – Matrix Multiplication and 2D Systolic Array Design – Systolic Design for Space Representations contain Delays

UNIT-IV:

Fast Convolution

Introduction – Cook-Toom Algorithm – Winograd algorithm – Iterated Convolution – Cyclic Convolution – Design of Fast Convolution algorithm by Inspection

UNIT-V:

Low Power Design

Scaling Vs Power Consumption –Power Analysis, Power Reduction techniques – Power Estimation Approaches

Programmable DSP: Evaluation of Programmable Digital Signal Processors, DSP Processors for Mobile and Wireless Communications, Processors for Multimedia Signal Processing.

TEXT BOOKS:

1. VLSI Digital Signal Processing- System Design and Implementation – Keshab K. Parhi, 1998, Wiley Inter Science.
2. VLSI and Modern Signal Processing – Kung S. Y, H. J. While House, T. Kailath, 1985, Prentice Hall.

REFERENCE BOOKS:

1. Design of Analog – Digital VLSI Circuits for Telecommunications and Signal Processing – Jose E. France, Yannis Tsividis, 1994, Prentice Hall.
2. VLSI Digital Signal Processing – Medisetti V. K, 1995, IEEE Press (NY), USA.

(PH2404115)NETWORK SECURITY AND CRYPTOGRAPHY

UNIT -I:

Introduction:

Attacks, Services and Mechanisms, Security attacks, Security services, A Model for Internetwork security. Classical Techniques: Conventional Encryption model, Steganography, Classical Encryption Techniques.

Modern Techniques:

Simplified DES, Block Cipher Principles, Data Encryption standard, Strength of DES, Differential and Linear Cryptanalysis, Block Cipher Design Principles and Modes of operations.

UNIT -II:

Encryption Algorithms:

Triple DES, International Data Encryption algorithm, Blowfish, RC5, CAST-128, RC2, Characteristics of Advanced Symmetric block ciphers. **Conventional Encryption :** Placement of Encryption function, Traffic confidentiality, Key distribution, Random Number Generation.

UNIT -III:

Public Key Cryptography: Principles, RSA Algorithm, Key Management, Diffie-Hellman Key exchange, Elliptic Curve Cryptography. **Number Theory:** Prime and Relatively prime numbers, Modular arithmetic, Fermat's and Euler's theorems, Testing for primality, Euclid's Algorithm, the Chinese remainder theorem, Discrete logarithms.

UNIT -IV:

Message Authentication and Hash Functions: Authentication requirements and functions, Message Authentication, Hash functions, Security of Hash functions and MACs. **Hash and Mac Algorithms**

MD File, Message digest Algorithm, Secure Hash Algorithm, RIPEMD-160, HMAC. Digital signatures and Authentication protocols: Digital signatures, Authentication Protocols, Digital signature standards.

Authentication Applications : Kerberos, X.509 directory Authentication service. Electronic Mail Security: Pretty Good Privacy, S/MIME.

UNIT -V:

IP Security:

Overview, Architecture, Authentication, Encapsulating Security Payload, Combining security Associations, Key Management. Web Security: Web Security requirements, Secure sockets layer and Transport layer security, Secure Electronic Transaction.

Intruders, Viruses and Worms

Intruders, Viruses and Related threats.

Fire Walls: Fire wall Design Principles, Trusted systems.

TEXT BOOKS:

1. Cryptography and Network Security: Principles and Practice - William Stallings, Pearson Education. First Edition 1995
2. Cryptography and Network Security: Principles and Practice – 8th Edition by Pearson 8th edition, Kindle Edition
3. Cryptography and Network Security: Atul Kahate, Mc Graw Hill, 3rd edition
4. Network Security Essentials (Applications and Standards) by William Stallings Pearson Education.

REFERENCE BOOKS:

1. Cryptography and Network Security by C.K.Shyamala, N Harini, and Dr.T.R.Padmanabhan published by Wiley India, First Edition, 2011
2. Cryptography and Network Security: Forouzan Mukhopadhyay, Mc Graw Hill, 3rd Edition 2011
3. Information security, principles and practice: Mark Stamp, Wiley India 2011.

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4. Principles of Computer Security: WM Arthur Conkin, Greg White, TMH,2007.
5. Fundamentals of Network Security by Eric Maiwald (Dreamtech press)
6. Network Security - Private Communication in a Public World by Charlie Kaufman, Radia Perlman and Mike Speciner, Pearson/PHI.
7. Principles of Information Security, Whitman, Thomson.
8. Network Security: The complete reference, Robert Bragg, Mark Rhodes, TMH
9. Introduction to Cryptography, Buchmann, Springer.

Video Lectures:

1. <http://nptel.ac.in/courses/106105031/> lecture by Dr debdeep Mukhopadhyay IIT Kharagpur
2. <https://ocw.mit.edu/coursesd/electrical-engineering-and-computer-science/6-033-computer-system-engineering-spring-2009/video-lectures/> lecture by Prof.Robert Morris and Prof.Samuel Madden MIT.

PAPER – II

(PH2404201)EMBEDDED SYSTEM DESIGN

UNIT-I: Introduction

An Embedded System-Definition, Examples, Current Technologies, Integration in system Design, Embedded system design flow, hardware design concepts, software development, processor in an embedded system and other hardware units, introduction to processor based embedded system design concepts.

UNIT-II: Embedded Hardware

Embedded hardware building blocks, Embedded Processors – ISA architecture models, Internal processor design, processor performance, Board Memory – ROM, RAM, Auxiliary Memory, Memory Management of External Memory, Board Memory and performance. Embedded board Input / output – Serial versus Parallel I/O, interfacing the I/O components, I/O components and performance, Board buses – Bus arbitration and timing, Integrating the Bus with other board components, Bus performance.

UNIT-III: Embedded Software

Device drivers, Device Drivers for interrupt-Handling, Memory device drivers, On-board bus device drivers, Board I/O drivers, Explanation about above drivers with suitable examples. Embedded operating systems – Multitasking and process Management, Memory Management, I/O and file system management, OS standards example – POSIX, OS performance guidelines, Board support packages, Middleware and Application Software – Middle ware, Middleware examples, Application layer software examples.

UNIT-IV: Embedded System Design, Development, Implementation and Testing

Embedded system design and development lifecycle model, creating an embedded system architecture, introduction to embedded software development process and tools- Host and Target machines, linking and locating software, Getting embedded software into the target system, issues in Hardware-Software design and co-design.

Implementing the design-The main software utility tool, CAD and the hardware, Translation tools, Debugging tools, testing on host machine, simulators, Laboratory tools, System Boot-Up.

UNIT-V: Embedded System Design-Case Studies

Case studies- Processor design approach of an embedded system –Power PC Processor based and Micro Blaze Processor based Embedded system design on Xilinx platform-NiosII Processor based Embedded system design on Altera platform-Respective Processor architectures should be taken into consideration while designing an Embedded System.

TEXT BOOKS:

1. Tammy Noergaard “Embedded Systems Architecture: A Comprehensive Guide for Engineers and Programmers”, Elsevier(Singapore) Pvt.Ltd.Publications, 2005.
2. Frank Vahid, Tony D. Givargis, “Embedded system Design: A Unified Hardware/Software Introduction”, John Wily & Sons Inc.2002.

REFERENCE BOOKS:

1. Peter Marwedel, “Embedded System Design”, Science Publishers, 2007.
2. Arnold S Burger, “Embedded System Design”, CMP.
3. Rajkamal, “Embedded Systems: Architecture, Programming and Design”, TMH Publications, Second Edition, 2008.

(PH2404202) WIRELESS COMMUNICATIONS AND NETWORKS

UNIT -I:

The Cellular Concept-System Design Fundamentals:

Introduction, Frequency Reuse, Channel Assignment Strategies, Handoff Strategies- Prioritizing Handoffs, Practical Handoff Considerations, Interference and system capacity – Co channel Interference and system capacity, Channel planning for Wireless Systems, Adjacent Channel interference, Power Control for Reducing interference, Trunking and Grade of Service, Improving Coverage & Capacity in Cellular Systems- Cell Splitting, Sectoring.

UNIT –II:

Mobile Radio Propagation: Large-Scale Path Loss:

Introduction to Radio Wave Propagation, Free Space Propagation Model, Relating Power to Electric Field, The Three Basic Propagation Mechanisms, Reflection-Reflection from Dielectrics, Brewster Angle, Reflection from perfect conductors, Ground Reflection (Two-Ray) Model, Diffraction-Fresnel Zone Geometry, Knife-edge Diffraction Model, Multiple knife-edge Diffraction, Scattering, Outdoor Propagation Models- Longley-Rice Model, Okumura Model, Hata Model, PCS Extension to Hata Model, Walfisch and Bertoni Model, Wideband PCS Microcell Model, Indoor Propagation Models-Partition losses (Same Floor), Partition losses between Floors, Log-distance path loss model, Ericsson Multiple Breakpoint Model, Attenuation Factor Model, Signal penetration into buildings, Ray Tracing and Site Specific Modeling.

UNIT –III:

Mobile Radio Propagation: Small –Scale Fading and Multipath

Small Scale Multipath propagation-Factors influencing small scale fading, Doppler shift, Impulse Response Model of a multipath channel- Relationship between Bandwidth and Received power, Small-Scale Multipath Measurements-Direct RF Pulse System, Spread Spectrum Sliding Correlator Channel Sounding, Frequency Domain Channels Sounding, Parameters of Mobile Multipath Channels-Time Dispersion Parameters, Coherence Bandwidth, Doppler Spread and Coherence Time, Types of Small-Scale Fading-Fading effects Due to Multipath Time Delay Spread, Flat fading, Frequency selective fading, Fading effects Due to Doppler Spread-Fast fading, slow fading, Statistical Models for multipath Fading Channels-Clarke's model for flat fading, spectral shape due to Doppler spread in Clarke's model, Simulation of Clarke and Gans Fading Model, Level crossing and fading statistics, Two-ray Rayleigh Fading Model.

UNIT -IV:

Equalization and Diversity

Introduction, Fundamentals of Equalization, Training A Generic Adaptive Equalizer, Equalizers in a communication Receiver, Linear Equalizers, Non-linear Equalization- Decision Feedback Equalization (DFE), Maximum Likelihood Sequence Estimation (MLSE) Equalizer, Algorithms for adaptive equalization-Zero Forcing Algorithm, Least Mean Square Algorithm, Recursive least squares algorithm. Diversity Techniques-Derivation of selection Diversity improvement, Derivation of Maximal Ratio Combining improvement, Practical Space Diversity Consideration-Selection Diversity, Feedback or Scanning Diversity, Maximal Ratio Combining, Equal Gain Combining, Polarization Diversity, Frequency Diversity, Time Diversity, RAKE Receiver.

UNIT -V:

Wireless Networks

Introduction to wireless Networks, Advantages and disadvantages of Wireless Local Area Networks, WLAN Topologies, WLAN Standard IEEE 802.11, IEEE 802.11 Medium Access

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Electronics and Communication Engineering**

Control, Comparison of IEEE 802.11 a,b,g and n standards, IEEE 802.16 and its enhancements, Wireless PANs, Hiper Lan, WLL.

TEXT BOOKS:

1. Wireless Communications, Principles, Practice – Theodore, S. Rappaport, 2nd Ed., 2002, PHI.
2. Wireless Communications-Andrea Goldsmith, 2005 Cambridge University Press.
3. Mobile Cellular Communication – Gottapu Sasibhushana Rao, Pearson Education, 2012.

REFERENCE BOOKS:

1. Principles of Wireless Networks – Kaveh Pah Laven and P. Krishna Murthy, 2002, PE
2. Wireless Digital Communications – Kamilo Feher, 1999, PHI.
3. Wireless Communication and Networking – William Stallings, 2003, PHI.
4. Wireless Communication – Upen Dalal, Oxford Univ. Press
5. Wireless Communications and Networking – Vijay K. Gary, Elsevier.

(PH2404203)IMAGE AND VIDEO PROCESSING

UNIT –I:

Fundamentals of Image Processing and Image Transforms:

Introduction, Image sampling, Quantization, Resolution, Image file formats, Elements of image processing system, Applications of Digital image processing

Introduction, Need for transform, image transforms, Fourier transform, 2 D Discrete Fourier transform and its transforms, Importance of phase, Walsh transform, Hadamard transform, Haar transform, slant transform Discrete cosine transform, KL transform, singular value decomposition, Radon transform, comparison of different image transforms.

UNIT –II:

Image Enhancement:

Spatial domain methods: Histogram processing, Fundamentals of Spatial filtering, Smoothing spatial filters, Sharpening spatial filters.

Frequency domain methods: Basics of filtering in frequency domain, image smoothing, image sharpening, Selective filtering.

Image Restoration:

Introduction to Image restoration, Image degradation, Types of image blur, Classification of image restoration techniques, Image restoration model, Linear and Nonlinear image restoration techniques, Blind deconvolution

UNIT –III:

Image Segmentation:

Introduction to image segmentation, Point, Line and Edge Detection, Region based segmentation., Classification of segmentation techniques, Region approach to image segmentation, clustering techniques, Image segmentation based on thresholding, Edge based segmentation, Edge detection and linking, Hough transform, Active contour

Image Compression:

Introduction, Need for image compression, Redundancy in images, Classification of redundancy in images, image compression scheme, Classification of image compression schemes, Fundamentals of information theory, Run length coding, Shannon – Fano coding, Huffman coding, Arithmetic coding, Predictive coding, Transformed based compression, Image compression standard, Wavelet-based image compression, JPEG Standards.

UNIT -IV:

Basic Steps of Video Processing:

Analog Video, Digital Video. Time-Varying Image Formation models: Three-Dimensional Motion Models, Geometric Image Formation, Photometric Image Formation, Sampling of Video signals, Filtering operations.

UNIT –V:

2-D Motion Estimation:

Optical flow, General Methodologies, Pixel Based Motion Estimation, Block- Matching Algorithm, Mesh based Motion Estimation, Global Motion Estimation, Region based Motion Estimation, Multi resolution motion estimation, Waveform based coding, Block based transform coding, Predictive coding, Application of motion estimation in Video coding.

TEXT BOOKS:

1. Digital Image Processing – Gonzaleze and Woods, 3rd Ed., Pearson.
2. Video Processing and Communication – Yao Wang, Joem Ostermann and Ya–quin Zhang. 1st Ed., PH Int.
3. S.Jayaraman, S.Esakkirajan and T.VeeraKumar, “Digital Image processing, Tata Mc Graw Hill publishers, 2009

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REFERENCE BOOKS:

1. Digital Image Processing and Analysis-Human and Computer Vision Application with CVIP Tools – Scotte Umbaugh, 2nd Ed, CRC Press, 2011.
2. Digital Video Processing – M. Tekalp, Prentice Hall International.
3. Digital Image Processing – S.Jayaraman, S.Esakkirajan, T.Veera Kumar – TMH, 2009.
4. Multidimensional Signal, Image and Video Processing and Coding – John Woods, 2nd Ed, Elsevier.
5. Digital Image Processing with MATLAB and Labview – Vipula Singh, Elsevier.
6. Video Demystified – A Hand Book for the Digital Engineer – Keith Jack, 5th Ed., Elsevier.

(PH2404204)OPTICAL COMMUNICATION TECHNOLOGY

UNIT –I:

Signal propagation in Optical Fibers:

Geometrical Optics approach and Wave Theory approach, Loss and Bandwidth, Chromatic Dispersion, Non Linear effects- Stimulated Brillouin and Stimulated Raman Scattering, Propagation in a Non-Linear Medium, Self-Phase Modulation and Cross Phase Modulation, Four Wave Mixing, Principle of Solitons.

UNIT –II:

Fiber Optic Components for Communication & Networking:

Couplers, Isolators and Circulators, Multiplexers, Bragg Gratings, Fabry-Perot Filters, Mach Zender Interferometers, Arrayed Waveguide Grating, Tunable Filters, High Channel Count Multiplexer Architectures, Optical Amplifiers, Direct and External Modulation Transmitters, Pump Sources for Amplifiers, Optical Switches and Wavelength Converters.

UNIT –III:

Modulation and Demodulation:

Signal formats for Modulation, Subcarrier Modulation and Multiplexing, Optical Modulations – Duobinary, Single Side Band and Multilevel Schemes, Ideal and Practical receivers for Demodulation, Bit Error Rates, Timing Recovery and Equalization, Reed-Solomon Codes for Error Detection and Correction.

UNIT -IV:

Transmission System Engineering:

System Model, Power Penalty in Transmitter and Receiver, Optical Amplifiers, Crosstalk and Reduction of Crosstalk, Cascaded Filters, Dispersion Limitations and Compensation Techniques.

UNIT –V:

Fiber Non-linearities and System Design Considerations:

Limitation in High Speed and WDM Systems due to Non-linearities in Fibers, Wavelength Stabilization against Temperature Variations, Overall System Design considerations – Fiber Dispersion, Modulation, Non-Linear Effects, Wavelengths, All Optical Networks.

TEXT BOOKS:

1. Optical Networks: A Practical Perspective - Rajiv Ramaswami and Kumar N. Sivarajan, 2nd Ed., 2004, Elsevier Morgan Kaufmann Publishers (An Imprint of Elsevier).
2. Optical Fiber Communications – Gerd Keiser, 3rd Ed., 2000, McGraw Hill.

REFERENCE BOOKS:

1. Optical Fiber Communications: Principles and Practice – John.M.Senior, 2nd Ed., 2000, PE.
2. Fiber Optics Communication – Harold Kolimbris, 2nd Ed., 2004, PEI
3. Optical Networks: Third Generation Transport Systems – Uyles Black, 2nd Ed., 2009, PEI
4. Optical Fiber Communications – Govind Agarwal, 2nd Ed., 2004, TMH.
5. Optical Fiber Communications and Its Applications – S.C.Gupta, 2004, PHI.

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(PH2404205) STATISTICAL SIGNAL PROCESSING

UNIT I

Signal models and characterization: Types and properties of statistical models for signals and how they relate to signal processing, Common second-order methods of characterizing signals including autocorrelation, partial correlation, cross-correlation, power spectral density and cross-power spectral density.

UNIT II

Spectral estimation: Nonparametric methods for estimation of power spectral density, autocorrelation, cross-correlation, transfer functions, and coherence from finite signal samples.

UNIT III

Review of signal processing: A review on random processes, A review on filtering random processes, Examples.

Statistical parameter estimation: Maximum likelihood estimation, maximum a posteriori estimation, Cramer-Rao bound.

UNIT IV

Eigen structure based frequency estimation: Pisarenko, MUSIC, ESPRIT their application sensor array direction finding.

Spectrum estimation: Moving average (MA), Auto Regressive (AR), Auto Regressive Moving Average (ARMA), Various non-parametric approaches.

UNIT V

Wiener filtering: The finite impulse case, causal and non-causal infinite impulse responses cases, Least mean squares adaptation, recursive least squares adaptation, Kalman filtering.

Text books:

1. Steven M. Kay, fundamentals of statistical signal processing: estimation Theory, Prentice-Hall, 1993.
2. Monsoon H. Hayes, Statistical digital signal processing and modeling, USA, Wiley, 1996.

Reference books:

1. Dimitris G. Manolakis, Vinay K. Ingle, and Stephen M. Kogon, Statistical and adaptive signal processing, Artech House, Inc, 2005, ISBN 1580536107

(PH2404206)PATTERN RECOGNITION PRINCIPLES

UNIT I : Introduction:

Fundamental problems in pattern Recognition system design, Design concepts and methodologies, Simple pattern recognition model.

Decisions and Distance Functions:

Linear and generalized decision functions, Pattern space and weight space, Geometrical properties, implementations of decision functions, Minimum-distance pattern classifications.

Probability - Probability of events:

Random variables, Joint distributions and densities, Movements of random variables, Estimation of parameter from samples.

UNIT - II: Decision making - Baye's theorem, Multiple features, Conditionally independent features, Decision boundaries, Unequal cost of error, estimation of error rates, the leaving-one-out-techniques, characteristic curves, estimating the composition of populations. Baye's classifier for normal patterns.

Non Parametric Decision Making:

histogram, kernel and window estimation, nearest neighbour classification techniques. Adaptive decision boundaries, adaptive discriminant functions, Minimum squared error discriminant functions, choosing a decision making techniques.

UNIT III: Clustering and Partitioning:

Hierarchical Clustering: Introduction, agglomerative clustering algorithm, the single-linkage, complete-linkage and average-linkage algorithm. Ward's method Partition clustering-Forg's algorithm, K-means's algorithm, Isodata algorithm.

UNIT IV: Pattern Preprocessing and Feature selection:

distance measures, clustering transformation and feature ordering, clustering in feature selection through entropy minimization, features selection through orthogonal expansion, binary feature selection.

UNIT V: Syntactic Pattern Recognition and Application of Pattern Recognition:

Concepts from formal language theory, formulation of syntactic pattern recognition problem, syntactic pattern description, recognition grammars, automata as pattern recognizers, Application of pattern recognition techniques in bio-metric, facial recognition, IRIS scan, Finger prints, etc.,

Text Books:

1. Pattern Recognition and image analysis by:Gose, Earl, published:1995,PHI
2. Pattern Recognition principles by Tou, Julis T.,1926-
3. Richard O.Duda, Peter E.Hart and David G. Stork , "Pattern Classification", 2nd Edition John Wiley, 2006
4. Pattern Recognition: An introduction " by J.T.K. O' Rourke Published by Addison-wesley , 1989 First Edition
5. C.M.Bishop, "Pattern Recognition and Machine Learning", Springer, 2009.
6. S.Theodoridis and K Koutroumbas, "pattern Recognition" 4th edition academic press-2009.

Reference books:

1. Pattern recognition and Image Analysis, Gose. Johnsonbaugh Jost, PHI.
2. Pattern Recognition Principle, Tou. Rafael. Gonzalez, Pea.
3. Pattern Classification, Richard duda, Hart., David Strok, Wiley.

UNIT –I:

Introduction to Mobile Computing Architecture:

Mobile Computing – Dialog Control – Networks – Middleware and Gateways – Application and Services – Developing Mobile Computing Applications – Security in Mobile Computing – Architecture for Mobile Computing – Three Tier Architecture – Design considerations for Mobile Computing – Mobile Computing through Internet – Making existing Applications Mobile Enabled.

UNIT –II:

Cellular Technologies: GSM, GPS, GPRS, CDMA and 3G:

Bluetooth – Radio Frequency Identification – Wireless Broadband – Mobile IP – Internet Protocol Version 6 (IPv6) – Java Card – GSM Architecture – GSM Entities – Call Routing in GSM – PLMN Interfaces – GSM addresses and Identifiers – Network aspects in GSM – Authentication and Security – Mobile computing over SMS – GPRS and Packet Data Network – GPRS Network Architecture – GPRS Network Operations – Data Services in GPRS – Applications for GPRS – Limitations of GPRS – Spread Spectrum technology – IS-95 – CDMA Versus GSM – Wireless Data – Third Generation Networks – Applications on 3G

UNIT –III:

Wireless Application Protocol (WAP) and Wireless LAN:

WAP – MMS – Wireless LAN Advantages – IEEE 802.11 Standards – Wireless LAN Architecture – Mobility in wireless LAN

Intelligent Networks and Interworking:

Introduction – Fundamentals of Call processing – Intelligence in the Networks – SS#7 Signaling – IN Conceptual Model (INCM) – soft switch – Programmable Networks – Technologies and Interfaces for IN

UNIT –IV:

Client Programming, Palm OS, Symbian OS, Win CE Architecture:

Introduction – Moving beyond the Desktop – A Peek under the Hood: Hardware Overview – Mobile phones – PDA – Design Constraints in Applications for Handheld Devices – Palm OS architecture – Application Development – Multimedia – Symbian OS Architecture – Applications for Symbian, Different flavors of Windows CE -Windows CE Architecture

J2ME:

JAVA in the Handset – The Three-prong approach to JAVA Everywhere – JAVA 2 Micro Edition (J2ME) technology – Programming for CLDC – GUI in MIDP – UI Design Issues – Multimedia – Record Management System – Communication in MIDP – Security considerations in MIDP – Optional Packages

UNIT –V:

Voice Over Internet Protocol and Convergence:

Voice over IP- H.323 Framework for Voice over IP – Session Initiation Protocol – Comparison between H.323 and SIP – Real Time protocols – Convergence Technologies – Call Routing – Voice over IP Applications – IP multimedia subsystem (IMS) – Mobile VoIP

Security Issues in Mobile Computing:

Introduction – Information Security – Security Techniques and Algorithms – Security Protocols – Public Key Infrastructure – Trust – Security Models – Security frameworks for Mobile Environment

TEXT BOOKS:

1. Mobile Computing – Technology, Applications and Service Creation – Asoke K Talukder, Roopa R Yavagal, 2009, TATA McGraw Hill
2. Mobile Communications – Jochen Schiller – 2nd Edition – Pearson Education

REFERENCE BOOKS:

1. The CDMA 2000 System for Mobile Communications – Vieri Vaighi, Alexander Damn Jaonvic – Pearson
2. Adalestein - Fundamentals of Mobile & Parvasive Computing, 2008, TMH

(PH2404208) PHASED ARRAY SYSTEMS

UNIT –I:

Conventional Scanning Techniques:

Mechanical versus electronic scanning, Techniques of Electronic scanning, Frequency, Phase and time delay scanning principle, Hybrid scanning techniques.

UNIT –II:

Array Theory:

Linear and Planar arrays, various grid configuration, Concept of cell and grid, Calculation of minimum number of elements, Radiation pattern, Grating lobe formation, Rectangular and triangular grid design of arrays.

UNIT –III:

Feed Networks for phased Arrays:

Corporate Feed, Lens and Reflect feed Techniques, Optimum f/d ratio basic building block for corporate feed network, Series, Parallel feed networks, Comparison of various feeding techniques, Antenna Array Architecture, Brick/ Tile Type construction.

UNIT –IV:

Frequency Scanned Array Design:

Snake feed, Frequency-phase scanning, Phase scanning, Digital phase shifter PIN diode and Ferrite phase shifters for phased arrays, Beam pointing errors due to digitalization, Beam pointing accuracy.

UNIT –V:

Search Patterns:

Calculation of search frame time, airborne phased array design, Electronic scanning radar parameter calculation, Application of phased arrays, Phased Array Radar Systems, Active Phased Array, TR/ATR Modules.

TEXT BOOKS:

1. Olliner, A.A, and G.H. Knittel - Phased Array Antennas, Artech House, 1972.
2. Kahrilas. PJ - Electronic Scanning Radar Systems Design Handbook, Artech House, 1976.

REFERENCE BOOKS:

1. Skolnik. MI- Radar Handbook, Mcgraw Hillso, NY, McGraw Hills-2007
2. Galati, G-(editor) - Advanced Radar Technique and Systems, Peter Peregrims Ltd, London, 1993.

(PH2404209)MICROWAVE NETWORKS

UNIT –I:

Introduction to Microwave Circuit Concept:

One port junction, Terminal voltage and currents in multipart junctions, Poynting's energy theorem, Normalized waves and scattering matrix. Properties of [s]matrix

UNIT –II:

Relationship between [s], [z] and [y] Parameters:

Wave amplitude transmission matrix[A], Relation between [A] and [s], [s] matrix of magic T, E and H plane tees, Directional coupler, Applications of hybrid junction and magic tee.

UNIT –III:

Passive Microwave Devices:

Even and odd mode analysis of symmetrical 4 port networks, Analysis and design of branch line couplers, Hybrid ring coupler, Frequency response, Branching synthesis of hybrids, Applications of hybrids.

UNIT –IV:

Microwave Propagation in Ferrites:

Principles of Faraday rotation, Isolator, Gyrator, Circulator, Phase shifters, S-matrix of non reciprocal devices, Broad band matching multisection quarter wave transformers, Binomial and chebyshev transformers design, Tapered transmission line exponential and triangular tapers, Synthesis of transmission line tapers.

UNIT –V:

Wave Analysis of Periodic Structures:

Image parameters method of micro wave filter design, Power loss ratio, Filter design by insertion loss method, Frequency transformation maximally flat and chebyshev filter design and characteristics.

TEXT BOOKS:

1. Altman JL -Microwave circuit, D van Nostrand Co.,Inc.,1964.
2. Collins. RE - Foundations for microwave engineering, John Wiley & Sons, inc 2nd Edn, 2009.

REFERENCE BOOKS:

1. Ghosh.RN - Microwave Circuit Theory and Analysis, McGraw Hill.1963
2. Pozar.D M - Microwave Engineering, 2nd Edn., John Wiley and Sons, Inc.,1999.

(PH2404210)RF CIRCUIT DESIGN

UNIT -I:

Introduction to RF Electronics:

The Electromagnetic Spectrum, units and Physical Constants, Microwave bands – RF behavior of Passive components: Tuned resonant circuits, Vectors, Inductors and Capacitors - Voltage and Current in capacitor circuits – Tuned RF / IF Transformers.

UNIT -II:

Transmission Line Analysis: Examples of transmission lines- Transmission line equations and Biasing- Micro Strip Transmission Lines- Special Termination Conditions- sourced and Loaded Transmission Lines. **Single And Multiport Networks:** The Smith Chart, Interconnectivity networks, Network properties and Applications, Scattering Parameters.

UNIT -III:

Matching and Biasing Networks:

Impedance matching using discrete components – Micro strip line matching networks, Amplifier classes of Operation and Biasing networks. **RF Passive & Active Components:** Filter Basics – Lumped filter design – Distributed Filter Design – Diplexer Filters- Crystal and Saw filters- Active Filters - Tunable filters – Power Combiners / Dividers – Directional Couplers – Hybrid Couplers – Isolators. RF Diodes – BJTs- FETs- HEMTs and Models.

UNIT -IV:

RF Transistor Amplifier Design: Characteristics of Amplifiers - Amplifier Circuit Configurations, Amplifier Matching Basics, Distortion and noise products, Stability Considerations, Small Signal amplifier design, Power amplifier design, MMIC amplifiers, Broadband High Power multistage amplifiers, Low noise amplifiers, VGA Amplifiers.

UNIT -V:

Oscillators: Oscillator basics, Low phase noise oscillator design, High frequency Oscillator configuration, LC Oscillators, VCOs, Crystal Oscillators, PLL Synthesizer, and Direct Digital Synthesizer. **RF Mixers:** Basic characteristics of a mixer - Active mixers- Image Reject and Harmonic mixers, Frequency domain considerations.

TEXT BOOKS:

1. RF Circuit design: Theory and applications by Reinhold Ludwig, Pavel Bretchko. Pearson Education Asia Publication, New Delhi 2001.
2. Radio Frequency and Microwave Communication Circuits – Analysis and Design – Devendra K. Misra, Wiley Student Edition, John Wiley & Sons, 2001.

REFERENCE BOOKS:

1. Radio frequency and Microwave Electronics - Mathew M.Radmangh, 2001, PE Asia Publisherts.
2. RF Circuit Design – Christopher Bowick, Cheryl Aljuni and John Biyler, Elsevier Science, 2008.
3. Secrets of RF Design - Joseph Carr., 3rd Edition, Tab Electronics.2001.
4. Complete Wireless Design - Cotter W. Sawyer, 2nd Edition, Mc-Graw Hill.2008.
5. Practical RF Circuit Design for Modem Wireless Systems Vol.2 -Less Besser and Rowan Gilmore.2003.

**Syllabus for Pre-Ph. D Examination
Electronics and Communication Engineering**

(PH2404211)CMOS MIXED SIGNAL CIRCUIT DESIGN

UNIT-I: Switched Capacitor Circuits

Introduction to Switched Capacitor circuits- basic building blocks, Operation and Analysis, Non-ideal effects in switched capacitor circuits, Switched capacitor integrators first order filters, Switch sharing, biquad filters.

UNIT-II: Phased Lock Loop (PLL)

Basic PLL topology, Dynamics of simple PLL, Charge pump PLLs-Lock acquisition, Phase/Frequency detector and charge pump, Basic charge pump PLL, Non-ideal effects in PLLs-PFD/CP non-idealities, Jitter in PLLs, Delay locked loops, applications.

UNIT-III: Data Converter Fundamentals

DC and dynamic specifications, Quantization noise, Nyquist rate D/A converters- Decoder based converters, Binary-Scaled converters, Thermometer-code converters, Hybrid converters

UNIT-IV: Nyquist Rate A/D Converters

Successive approximation converters, Flash converter, Two-step A/D converters, Interpolating A/D converters, Folding A/D converters, Pipelined A/D converters, Time-interleaved converters.

UNIT-V: Oversampling Converters

Noise shaping modulators, Decimating filters and interpolating filters, Higher order modulators, Delta sigma modulators with multibit quantizers, Delta sigma D/A

TEXT BOOKS:

1. Design of Analog CMOS Integrated Circuits- Behzad Razavi, TMH Edition, 2002
2. CMOS Analog Circuit Design - Philip E. Allen and Douglas R. Holberg, Oxford University Press, International Second Edition/Indian Edition, 2010.
3. Analog Integrated Circuit Design- David A. Johns,Ken Martin, Wiley Student Edition, 2013

REFERENCE BOOKS:

1. CMOS Integrated Analog-to- Digital and Digital-to-Analog converters-Rudy Van De Plassche, Kluwer Academic Publishers, 2003
2. Understanding Delta-Sigma Data converters-Richard Schreier, Wiley Interscience, 2005.
3. CMOS Mixed-Signal Circuit Design - R. Jacob Baker, Wiley Interscience, 2009.

(PH2404212) SEMICONDUCTOR MEMORY DESIGN AND TESTING

UNIT-I: Random Access Memory Technologies

SRAM – SRAM Cell structures, MOS SRAM Architecture, MOS SRAM cell and peripheral circuit operation, Bipolar SRAM technologies, SOI technology, Advanced SRAM architectures and technologies, Application specific SRAMs, DRAM – DRAM technology development, CMOS DRAM, DRAM cell theory and advanced cell structures, BICMOS DRAM, soft error failure in DRAM, Advanced DRAM design and architecture, Application specific DRAM.

UNIT-II: Non-volatile Memories

Masked ROMs, High density ROM, PROM, Bipolar ROM, CMOS PROMS, EPROM, Floating gate EPROM cell, One time programmable EPROM, EEPROM, EEPROM technology and architecture, Non-volatile SRAM, Flash Memories (EPROM or EEPROM), advanced Flash memory architecture

UNIT-III: Memory Fault Modeling Testing and Memory Design for Testability and Fault Tolerance

RAM fault modeling, Electrical testing, Pseudo Random testing, Megabit DRAM Testing, non-volatile memory modeling and testing, IDDQ fault modeling and testing, Application specific memory testing, RAM fault modeling, BIST techniques for memory

UNIT-IV: Semiconductor Memory Reliability and Radiation Effects

General reliability issues RAM failure modes and mechanism, Non-volatile memory reliability, reliability modeling and failure rate prediction, Design for Reliability, Reliability Test Structures, Reliability Screening and qualification, Radiation effects, Single Event Phenomenon (SEP), Radiation Hardening techniques, Radiation Hardening Process and Design Issues, Radiation Hardened Memory characteristics, Radiation Hardness Assurance and Testing, Radiation Dosimetry, Water Level Radiation Testing and Test structures

UNIT-V: Advanced Memory Technologies and High-density Memory Packing Technologies

Ferroelectric RAMs (FRAMs), GaAs FRAMs, Analog memories, magneto resistive RAMs (MRAMs), Experimental memory devices, Memory Hybrids and MCMs (2D), Memory Stacks and MCMs (3D), Memory MCM testing and reliability issues, Memory cards, High Density Memory Packaging Future Directions.

TEXT BOOKS:

1. Semiconductor Memories Technology – Ashok K. Sharma, 2002, Wiley.
2. Advanced Semiconductor Memories – Architecture, Design and Applications - Ashok K. Sharma- 2002, Wiley.
3. Modern Semiconductor Devices for Integrated Circuits – Chenming C Hu, 1st Ed., Prentice Hall.

(PH2404213)DESIGN FOR TESTABILITY

UNIT-I:

Introduction to Testing

Testing Philosophy, Role of Testing, Digital and Analog VLSI Testing, VLSI Technology Trends affecting Testing, Types of Testing, Fault Modeling: Defects, Errors and Faults, Functional Versus Structural Testing, Levels of Fault Models, Single Stuck-at Fault.

UNIT-II:

Logic and Fault Simulation

Simulation for Design Verification and Test Evaluation, Modeling Circuits for Simulation, Algorithms for True-value Simulation, Algorithms for Fault Simulation.

UNIT -III:

Testability Measures

SCOAP Controllability and Observability, High Level Testability Measures, Digital DFT and Scan Design: Ad-Hoc DFT Methods, Scan Design, Partial-Scan Design, Variations of Scan.

UNIT-IV:

Built-In Self-Test

The Economic Case for BIST, Random Logic BIST: Definitions, BIST Process, Pattern Generation, Response Compaction, Built-In Logic Block Observers, Test-Per-Clock, Test-Per-Scan BIST Systems, Circular Self Test Path System, Memory BIST, Delay Fault BIST.

UNIT-V:

Boundary Scan Standard

Motivation, System Configuration with Boundary Scan: TAP Controller and Port, Boundary Scan Test Instructions, Pin Constraints of the Standard, Boundary Scan Description Language: BSDL Description Components, Pin Descriptions.

TEXT BOOKS:

1. Essentials of Electronic Testing for Digital, Memory and Mixed Signal VLSI Circuits - M.L. Bushnell, V. D. Agrawal, Kluwer Academic Publishers.

REFERENCE BOOKS:

1. Digital Systems and Testable Design - M. Abramovici, M.A.Breuer and A.D Friedman, Jaico Publishing House.
2. Digital Circuits Testing and Testability - P.K. Lala, Academic Press.

(PH2404214)CPLD AND FPGA ARCHITECTURES AND APPLICATIONS

UNIT-I: Introduction to Programmable Logic Devices

Introduction, Simple Programmable Logic Devices – Read Only Memories, Programmable Logic Arrays, Programmable Array Logic, Programmable Logic Devices/Generic Array Logic; Complex Programmable Logic Devices – Architecture of Xilinx Cool Runner XCR3064XL CPLD, CPLD Implementation of a Parallel Adder with Accumulation.

UNIT-II: Field Programmable Gate Arrays

Organization of FPGAs, FPGA Programming Technologies, Programmable Logic Block Architectures, Programmable Interconnects, Programmable I/O blocks in FPGAs, Dedicated Specialized Components of FPGAs, Applications of FPGAs.

UNIT-III: SRAM Programmable FPGAs

Introduction, Programming Technology, Device Architecture, The Xilinx XC2000, XC3000 and XC4000 Architectures.

UNIT-IV: Anti-Fuse Programmed FPGAs

Introduction, Programming Technology, Device Architecture, The Actel ACT1, ACT2 and ACT3 Architectures.

UNIT-V: Design Applications

General Design Issues, Counter Examples, A Fast Video Controller, A Position Tracker for a Robot Manipulator, A Fast DMA Controller, Designing Counters with ACT devices, Designing Adders and Accumulators with the ACT Architecture.

TEXT BOOKS:

1. Field Programmable Gate Array Technology - Stephen M. Trimberger, Springer International Edition.
2. Digital Systems Design - Charles H. Roth Jr, Lizy Kurian John, Cengage Learning.

REFERENCE BOOKS:

1. Field Programmable Gate Arrays - John V. Oldfield, Richard C. Dorf, Wiley India.
2. Digital Design Using Field Programmable Gate Arrays - Pak K. Chan/Samiha Mourad, Pearson Low Price Edition.
3. Digital Systems Design with FPGAs and CPLDs - Ian Grout, Elsevier, Newnes.
4. FPGA based System Design - Wayne Wolf, Prentice Hall Modern Semiconductor Design Series.

(PH2404215)MICRO ELECTRO MECHANICAL SYSTEM DESIGN

UNIT-I: Introduction

Basic structures of MEM devices – (Canti-Levers, Fixed Beams diaphragms). Broad Response of Micro electromechanical systems (MEMS) to Mechanical (Force, pressure etc.) Thermal, Electrical, optical and magnetic stimuli, compatibility of MEMS from the point of power dissipation, leakage etc.

UNIT-II: Review

Review of mechanical concepts like stress, strain, bending moment, deflection curve. Differential equations describing the deflection under concentrated force, Distributed force, distributed force, Deflection curves for canti-levers- fixed beam. Electrostatic excitation – columbic force between the fixed and moving electrodes. Deflection with voltage in C.L, Deflection Vs Voltage curve, critical fringe field – field calculations using Laplace equation. Discussion on the approximate solutions – Transient response of the MEMS.

UNIT-III: Types

Two terminal MEMS - capacitance Vs voltage Curve – Variable capacitor. Applications of variable capacitors. Two terminal MEM structures. Three terminal MEM structures – Controlled variable capacitors – MEM as a switch and possible applications.

UNIT-IV: MEM Circuits & Structures

MEM circuits & structures for simple GATES- AND, OR, NAND, NOR, Exclusive OR, simple MEM configurations for flip-flops triggering applications to counters, converters. Applications for analog circuits like frequency converters, wave shaping. RF Switches for modulation. MEM Transducers for pressure, force temperature. Optical MEMS.

UNIT-V: MEM Technologies

Silicon based MEMS- Process flow – Brief account of various processes and layers like fixed layer, moving layers spacers etc., and etching technologies.

Metal Based MEMS: Thin and thick film technologies for MEMS. Process flow and description of the processes, Status of MEMS in the current electronics scenario.

TEXT BOOKS:

1. MEMS Theory, Design and Technology - GABRIEL. M.Review, R.F.,2003, John wiley & Sons. .
2. Strength of Materials –Thimo Shenko, 2000, CBS publishers & Distributors.
3. MEMS and NEMS, Systems Devices; and Structures - Servey E.Lyshevski, 2002, CRC Press.

REFERENCE BOOKS:

1. Sensor Technology and Devices - Ristic L. (Ed) , 1994, Artech House, London.